



09/293188

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Zhiping Yin et al.

Examiner: Phat X Cao

Patent No.: 7,279,414

Group Art Unit: 2814

Issue Date: October 9, 2007

Docket No: 1997-1121.01/US

Title: METHOD OF FORMING INTERCONNECT STRUCTURE WITH INTERLAYER DIELECTRIC

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch

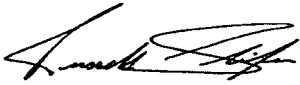
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- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
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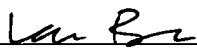
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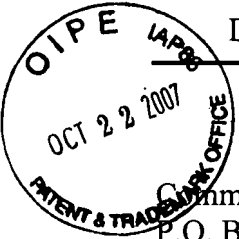
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DIELECTRIC



REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
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It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

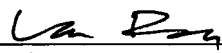
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,279,414

Page (1) of 1

DATED : October 9, 2007

INVENTOR(S) : Yin et al.

It is certified that error appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 24, in Claim 6, delete "filing" and insert - - filling - -, therefor.

OCT 9 2007